



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/776,553
Filing Date February 10, 2004
Inventor Weimin Li et al.
Assignee Micron Technology, Inc.
Group Art Unit 1755
Examiner Carol M. Koslow
Attorney's Docket No. MI22-2497
Title: Compositions of Matter and Barrier Layer Compositions

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

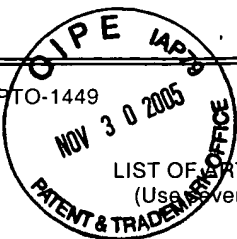
References -See Attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 CFR §1.56. Pursuant to FEDERAL REGISTER, Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art are attached. No admission is made regarding whether the listed references are prior art.

Respectfully submitted,

Dated: November 30, 2005

By: Jennifer J Taylor
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Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. MI22-2497	SERIAL NO. 10/776,553
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)		APPLICANT Weimin Li et al.	
		FILING DATE February 10, 2004	GROUP 1755

U.S. PATENT DOCUMENTS

*Examiner Initial	Document Number	Date	Name	Class	Sub-class	Filing Date If Appropriate
AA	5,840,610	11/1998	Gilmer et al.			
AB	4,523,214	06/1985	Hirose et al.			
AC	6,432,791 B1	08/2002	Hutter et al.			
AD	6,017,614	01/2000	Tsai et al.			
AE	6,057,217	05/2000	Uwasawa			
AF	6,200,863 B1	03/2001	Xiang et al.			
AG	4,592,129	06/1986	Legge			
AH	6,218,292	04/2001	Foote			
AI	2003/0077916	04/2003	Xu et al.			
AJ	2003/0207594	11/2003	Catabay et al.			
AK	2004/0071878	04/2004	Schuhmacher			

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Sub-class	Translation	
						Yes	No
AN	JP8051058	02/1996	Japan				
AO	JP63316476	12/1988	Japan				
AP	TW 47112A	01/2002	Taiwan				
AQ	JP8078322	03/1996	Japan				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

AR	Stanley Wolf, SILICON PROCESSING FOR THE VLSI ERA, Vol. 3, Lattice Press 1995, page 635.
AS	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Volume 1, Pages 1 and 2.

EXAMINER	DATE CONSIDERED
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.